Icemos Technology Ltd Product Specification 1003.290101 Issue Date 03 July 2014 17:02:57

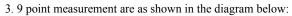
Part Number	Customer	

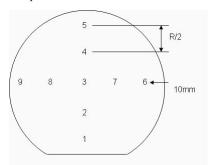
Category		Parameter	Specification	Measurement Method
OverallWafer	1.0	Diameter	150.00 +/- 0.50 mm	WaferVendor
	2.0	Primary Flat Length	57.50 +/- 2.50 mm	Wafer Vendor
	3.0	Primary Flat Orientation	{110}+/-1 degree	Wafer Vendor
	4.0	Growth Method	CZ	Wafer Vendor
	5.0	Туре	N	Wafer Vendor
	6.0	Dopant	Phosphorous	Wafer Vendor
	7.0	Resistivity	1~ 10 ohm cm	Wafer Vendor
	8.0	Overall Thickness	300.00 +/- 5.00 um	ADE
	9.0	Total Thickness Variation (TTV)	<3.00um	Guaranteed by process
	10.0	Bow	<50.00um	ADE to ASTM F534, 20%
	11.0	Warp	<50.00um	ADE to ASTM F657, 20%
	12.0	Orientation	<100> +/-0.5	Wafer Vendor
	13.0	Back Surface Quality	Polished	Wafer Vendor
	14.0	Lasermarking	Backside, Format: PPT-PND-XXXX-XX	Guaranteed by process, see additional notes.
	15.0	Front Surface Quality	Prime	Wafer Vendor
	16.0	Edge Chips	None	Bright Light 100% (note 2)
	17.0	Edge Rounding	R130/22deg	Wafer Vendor
HandleSilicon	18.0	Handle Thickness	300.00 +/- 5.00 um	ADE
DeviceSilicon	19.0	Haze	None	Bright Light, 100% (note 2).
	20.0	Scratches	None	Bright Light, 100% (note 2).

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Part Number		Customer	
Category	Parameter	Specification	Measurement Method
Shipping Details	Wafer per box :	Max 25	
	Packaging:	Taped Polypropylene Wafer Box Empak, Ultrapak, 150.00mm Antistatic Double Bagging	
	Lot Shipment Data	Device Thickness Bow / Warp Data Handle and SOI Thickness	
Explanatory Notes	1. Microscope inspec	etion performed using microscope scan as below. 5x objective.	
	2. All bright light ins	pections performed exclude all wafer area outside the edge exclusion	on defined in Overall

2. All bright light inspections performed exclude all wafer area outside the edge exclusion defined in Overall Wafer, Edge Exclusion. High intensity bright lamp inspection as per ASTM F523.





Additional Information